# Benchmarking Computer Systems for Robot Control

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Abstract—The high computational burden posed by modern control algorithms often precludes their industrial application using present day microcomputers. In this paper we evaluate the computational load of different logical and arithmetic operations and the capabilities of several computing systems (software and hardware). Real-time limitations are alleviated through the adoption of general techniques associated with the data representation. Such techniques achieve not only a more efficient management of the computational resources but also provide a deeper insight on developments toward future computer control architectures.

#### I. INTRODUCTION

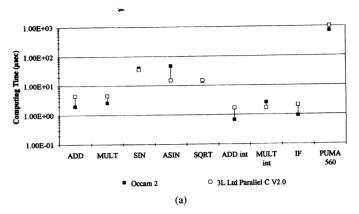
OBOT manipulators are mechanical systems that have several links connected through rotational or prismatic joints. These devices have complex kinematic and dynamic phenomena that require efficient controllers. Present day industrial robots use linear PID controllers; however, they are inappropriate for high performance applications because they lead to limited path tracking accuracy and often exhibit vibrations at high speeds. The low efficiency of these systems motivated the appearance of controllers based on different concepts [1]-[5]. However, the high computational burden posed by many of these algorithms precludes their industrial application using present day microcomputers. Moreover, powerful monoprocessor systems may be expensive while multi-microprocessor architectures [6]-[10] are still in a research stage. These limitations require the development of control strategies more adapted to microcomputer-based structures. In this line of thought, we may question the feasibility of the implementation of a given algorithm and which are the most adequate techniques to do the job. On the other hand, the technical literature is scarce on the evaluation of the computational load posed by each algorithm and its dependence on the software and hardware structure.

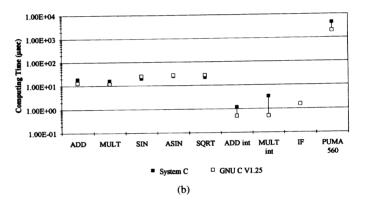
This paper compares the capabilities of several computer systems and introduces techniques that render more efficient practical implementations. Section II evaluates the computational load of different logical and arithmetic operations of several computer systems. Section III outlines general techniques amenable to practical implementations and Section IV presents the conclusions.

#### II. EVALUATION OF THE COMPUTER SYSTEMS

The evaluation of the computational load required by an algorithm and the capabilities of a given computational system, are essential steps in any preliminary study regarding its development. The literature suggests many control algorithms;

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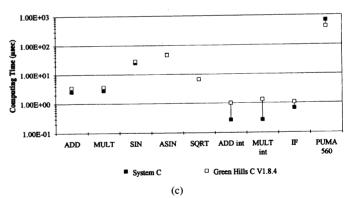


Fig. 1. Computing time  $\tau$  of the PUMA 560 simplified dynamic equations using different compilers for (a) IMS T800–20; (b) SUN 3; (c) AViiON AVX 300

however, in most cases, those proposing an algorithm do not analyze the computational requirements associated with its implementation. Some studies take into account the computational load based on the required number of additions and multiplications, while others only mention the maximum sampling frequencies achieved after installing the algorithm on a given system. Obviously such approaches are far from satisfactory, because they do not consider all the "factors"

TABLE I COMPUTATION TIMES FOR SEVERAL SYSTEMS

Computer	IBM PS/2	IBM PS/2	IBM PS/2	IBM PS/2	IBM PS/2	IBM PS/2	IBM PS/2	IBM PS/2	IBM PS/2
OS	MsDOS	MsDOS	MsDOS	MsDOS	MsDOS	MsDOS	MsDOS	MsDOS	MsDOS
Clock (MHz)	8	10	16	25	8	10	16	25	25
Processor	8086	80286	80386 SX	80386	8086/8087	80286/80287	80386 SX/ /80387 SX	80386/80387	80486
Programming	TC V2.0	TC V2.0	TC V2.0	TC V2.0	TC V2.0	TC V2.0	TC V2.0	TC V2.0	TC V2.0
Language									
Operation	Computation Time (µsec)								
Add fp	540 - 660	223 - 251	181 - 202	84 - 95	52 - 55	46 - 50	20 - 24	9 - 11	1.7 - 3.7
Mult fp	870 -940	293 - 299	225 - 230	110 - 114	57 - 63	51 - 53	22 - 24	9 - 11	1.7 - 3.7
Sin	3500 - 4300	939 - 1175	697 - 884	335 - 418	300 - 360	236 269	54 - 66	27 - 39	11 - 22
Asin	6580 - 9560	1856 - 2802	1389 - 2158	664 - 1011	220 - 305	181 - 253	82 - 121	43 - 66	21 - 28
Sgrt	1220 - 1540	392 - 432	317 - 348	150 - 163	76 - 80	58 - 61	31 - 33	14 - 17	5.3 - 7.3
Add int	8.3	4.3	2.9	1.2	8.3	4.3	2.9	1.2	0.5
Mult int	22.5	5.2	3.2	1.5	22.5	5.2	3.2	1.5	1.1
If	9.1	4.5	2.8	1.1	9.1	4.5	2.8	1.1	0.47

Computer	Apollo	SUN 3	SUN 4	SUN 4	AViiON	Next Cube	DecStation	IBM 6000	Transputer
1	DN 3500			SparcSt. 1	AVX 300		3100	St. 530	T800-20
OS	BSD 4.2	UNIX 4.2	UNIX 4.3.2	SUN 4.0.3	DG/UX 4.2	Nextstep 2.1	Ultrix 3.1	AIX V3.1	
1	Do/IX					Mach 2.5			
Clock (MHz)	25	20	14.3	20	16.7	25	16.7	25	20
Processor	68030/68882	68020/68881	SPARC	SPARC	88100	68040	MIPS	IBM Power	
							2000/2010	System 6000	
Programming	System C	System C	System C	System C	System C	System C	System C	System C	Occam 2
Language									
Operation		Computation Time (µsec)							
Add fp	7.3 - 7.5	18 - 19	3.0 - 4.0	1.9 - 2.1	2.2 - 2.5	0.3 - 0.6	0.16 - 0.33	0.13 - 0.15	1.8 - 2.0
Mult fp	7.3 - 7.5	15 - 16	3.0 - 4.0	2.1 - 2.2	2.3 - 2.8	0.3 - 0.6	0.16 - 0.33	0.13 - 0.14	2.5 - 2.6
Sin	23 - 25	17 - 20	5.0 - 7.0	4.0 - 4.3	22 - 25	6 - 7	5.0 - 8.3	2.3 - 3.2	39 - 41
Asin	143 - 145	22 - 25	27 - 32	14.7 - 15.5	41 - 48	6 - 8	8.3 - 10.0	1.8 - 1.8	30 - 47
Sqrt	26.7 - 28.3	15 - 22	3.0 - 7.0	3.3 4.0	3.3 - 6.7	3 - 4	1.6 - 1.7	3.8 - 3.9	13 - 14
Add int	0.42	1.2	0.87	0.63	0.27	0.08	0.024	0.013	0.66
Mult int	0.58	3.3	2.8	1.7	0.27	0.08	0.024	0.013	2.54
If	1.3	1.6	1.2	6.2	0.67	0.36	0.056	0.034	0.92

TC - Borland Turbo C V2.0, fp. floating point, int: integer, Precision of the calculations- fp: 8 bytes, int: 4 bytes

involved. The difficulty of the problem lies precisely in the large number of factors involved, such as the type of microprocessor, clock frequency, memory wait states, type and version of the compiler, type and accuracy of the operations, etc. Although not considering all possible combinations the data displayed in Table I attempts to clarify these issues [11], [33], [34]. This data corresponds to the range of variation of the computational time  $\tau$  required by each arithmetic or logical operation for a given system (software and hardware). Among the large number of possibilities we have chosen those combinations more relevant in controller implementation. Inspecting the data we can draw several conclusions:

- Trigonometric operations are the most time consuming.
- Logical and integer (int) arithmetic operations are the fastest.
- The arithmetic coprocessor is essential to speed-up floating point (fp) operations.
- The presence or absence of coprocessors does not affect the computing time of logical or integer arithmetic operations.
- For a given hardware, large variations of computing time may occur depending on the compiler being used (Fig. 1).
- Theoretically, the speed of calculations increases linearly with the clock frequency. However, for large frequencies, wait states may occur when accessing memory [12].
- Reduced Instruction Set Computer (RISC) architectures appear to be far more efficient than conventional Complex Instruction Set Computers (CISC).

In order to provide a better perspective of these properties, we have decided to measure the frequency of calculation for an adequate benchmark. Because extrapolation from generic

benchmarks is questionable [12]-[14], we have decided to select a benchmark capable of reflecting the requirements normally associated with robot control. In Fig. 2 we show the frequencies f of calculation of the inverse dynamics for the six DOF PUMA 560 (Table II) manipulator versus the index  $\mu = f/f_{clock}$  for the computer systems mentioned in Table I. The inverse dynamics equations include not only the simplifications presented in [15] but also some improvements introduced by the authors, which reduced the number of required operations to 11 sines/cosines, 89 additions and 150 multiplications. In the chart of Fig. 2, f represents an "absolute" computation frequency while  $\mu$  corresponds to a "normalized frequency" because it is independent of the clock frequency  $f_{clock}$ . The results reveal that the computation speed-up through the acceleration of  $f_{clock}$ , which is, essentially, technological dependent, is less significant than the improvement attained by the optimization of the computer architecture. Furthermore, the order of magnitude of the results demonstrates that the implementation of a controller containing algorithms such as the kinematics, the dynamics, and the trajectory planning can easily reach a computational load incompatible with the use of high sampling frequencies. The development of techniques for the real-time implementation of these algorithms is the matter of the next section.

## III. TECHNIQUES TO IMPROVE THE PERFORMANCES OF COMPUTER CONTROL SYSTEMS

Modern robot control algorithms pose very stringent computational requirements. Although technological progress makes available systems with ever increasing performances, the truth

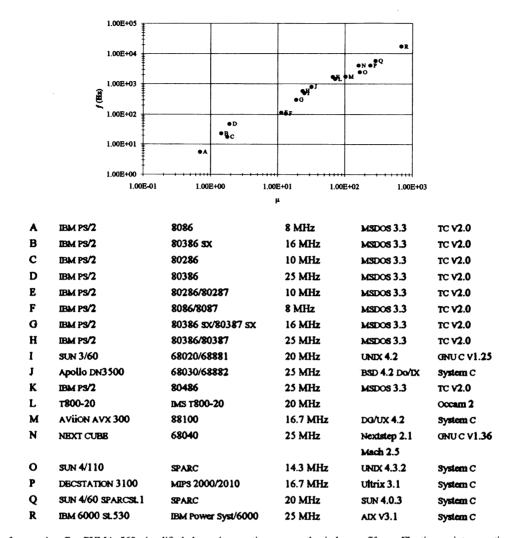


Fig. 2. Computing frequencies ffor PUMA 560 simplified dynamic equations vrsus the index  $\mu = ff_{clock}$ . Floating point operations are evaluated using the standard precision of 8 bytes (refer to legend above.)

is that their use as robot controllers may not be economically feasible. In the next four subsections we present a set of techniques to improve the real-time performances of the control system, which are, to a large extent "hardware independent."

#### A. Assembly Language Programming

This is a well-known technique and constitutes a natural starting point to improve the real-time performances of any controller. Direct programming in assembly language allows considerable optimization of the generated code. However, modern control algorithms are very complex and that makes their programming in assembly very time consuming. This is why only a few researchers have adopted this strategy [16]–[18] as an alternative to high level languages such as FORTRAN or C.

#### B. Low Precision Arithmetic Calculations

This option has been one of the principal alternatives to assembly language. The most common technique consists in giving up floating point calculations in favor of fixed point arithmetic [19]-[20]. At first sight it would seem more

practical to reduce only the accuracy of the fp calculations. However, this is somewhat deceptive because many of the high level languages implement low accuracy fp operations based on operations with standard accuracy. Therefore, and contrary to our expectations, there is no improvement of the computation times. Nevertheless, computation can be faster as long as the high level language has a distinct mathematical library for each accuracy. Fig. 3 shows the computational speed-up for several fp arithmetic operations on the IMS T800-20 transputer (source code: Occam 2). Using, again, the PUMA 560 dynamics as our benchmark, we measure f = 1.4 KHz ( $\mu = 72$ ) and f = 2.3 KHz ( $\mu = 114$ ) for 8 and 4 byte fp precision, respectively, that corresponds to a speed-up factor of 1.6.

In the same philosophy we can also mention the evaluation of transcendental functions through polynomial approximations. H. Henrichfreise [31] reports the approximation of the sine/cosine functions by a 9th/10th least square polynomials fit and the adoption of 32-bit (24-bit mantissa) fp format for robot control, giving a speed-up of the computing time from  $\tau = 4.5$   $\mu$ sec to  $\tau = 1.7$   $\mu$ sec. Using these techniques [32], the PUMA 560 dynamic equations were computed on a TMS320C30-33

TABLE II
SIMPLIFIED DYNAMIC EQUATION OF THE ROBOT PUMA 560

```
= Cos(q2)
S_3 = Sin(q_3)
C_3 = Cos(q_3)
 S_4 = Sin(q_4)
 C. = Cog(q.)
 S_5 = Sin(q_5)
C_4 = Cos(q_5)
q_{23} = q_2 + q_3

S_{23} = Sin(q_{23})

C_{23} = Coo(q_{23})
  C_{223} = Cos(q_2 + q_{23})
 a_1 = S_{23} * S_{23}
\alpha_1 = S_{23} * C_{23}
\alpha_2 = S_{23} * C_{23}
\alpha_3 = C_2 * S_{23}
\alpha_4 = C_2 * C_{23}
 \alpha_5 = S_4 * S_5
\alpha_6 = C_4 * S_5
 a, = S, + C,
                                                                                                                                    β11 = q3 + q4
 \beta_1 = \dot{q}_1 * \dot{q}_1
                                                                   β6 = q2 + q2
                                                                                                                                     β<sub>12</sub> = q, • q,
                                                                   β<sub>7</sub> = q<sub>2</sub> + q<sub>3</sub>
 \beta_2 = q_1 \cdot q_2
 \beta_3 = \dot{q}_1 * \dot{q}_3
 β4 = q1 + q4
                                                                   \beta_9 = q_2 \cdot q_5
                                                                                                                                     β14 = Q4 + Q5
                                                                    β10 = 43 + 43
                                                                                                                                     β15 = q5 + q5
 \beta_5 = \dot{q}_1 * \dot{q}_5
  \chi_1 = \beta_7 + 0.5 + \beta_{10}
 \chi_2 = \beta_8 + \beta_{11}
 \chi_3 = \beta_9 + \beta_{12}
       = -0.134 • C<sub>23</sub>
  \zeta_3 = -0.0025 * S_4
   \zeta_4 = [0.00164 + 0.0003 + (1 - 2 + S_4 + S_4)] + S_{23} - 0.0025 + C_{23} + \alpha_6
  \zeta_5 = -(0.0025 * C_5 + 0.000642) * C_{23} * S_4
\zeta_6 = 0.6 * \alpha_2 - 0.0213 * (1 - 2 * \alpha_1)
  \zeta_7 = 0.022 * S_3 + 0.744 * C_3
\zeta_4 = 0.267 * S_{23} - 0.00758 * C_{23}
\zeta_9 = \zeta_3 + 0.00248 * (C_3 * \alpha_8 - S_3 * S_5)
  Inertials Coefficients
  D[1,1] = 2.57 + 1.38 * C_2 * C_2 + 0.3 * \alpha_1 + 0.744 * \alpha_3
```

MHz, running on a dSPACE DS1002 hardware system (source code: Texas Instruments C Vs. 4.40) at a rate of f = 23 KHz ( $\mu = 705$ ).

#### C. Use of Memory

 $D[1,2] = 0.69 * S_2 + \zeta_2 + 0.0238 * C_2$ 

This method transfers, in part or entirely, the load of the on-line calculations to memory-based evaluations. A common procedure consists of replacing the fp calculations of trigonometric functions by memory tables [17], [20]. This method can be generalized to a greater portion of the algorithm; however, the application of this technique to control algorithms has not received much attention to date with the exception of controllers based on learning strategies [21]–[26]. Therefore, a large field of research remains open.

#### D. Multirate Schemes

The effects of the sampling frequency upon the performances of a digital controller are very important. In a robot system having feedback and feedforward paths, it is natural to expect different speeds of response along them. Therefore, it is reasonable to allocate different sampling (and computing) rates to such paths. The multirate schemes [27]–[29] assign the computing power to each loop in accordance to its needs leading to a more rational management of the

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TABLE II (CONTINUED)
SIMPLIFIED DYNAMIC EQUATION OF THE ROBOT PUMA 560
```

 $\begin{aligned} &D[1,3] = \zeta_2 - 0.00397 * S_{23} \\ &D[2,2] = 6.79 + 0.744 * S_3 \\ &D[2,3] = 0.333 + 0.372 * S_3 - 0.011 * C_3 \end{aligned}$ 

D[3,3] = 1.16

```
D[3,4] = -0.00125 * \alpha_5
D[3,5] = 0.00125 * \alpha_0
D[4,4] = 0.2

D[5,5] = 0.18
D[6,6] = 0.19
Coriolis/Contrinetal Coeffici
\delta_1 = -2.76 * S_2 * C_2 + 0.744 * C_{223} + \zeta_6
 \delta_3 = 0.744 * \alpha_4 + 0.022 * \alpha_3 + \zeta_6
\begin{array}{l} \delta_3=0, \dots = \omega_4 \\ \delta_4=0, 5+\delta_3 \\ \delta_5=(-0.0025+\alpha_2+0.00086+\alpha_4-0.00248+\alpha_4)+\alpha_5 \\ \delta_6=-0.0025+(\alpha_1+S_3-\alpha_2+\alpha_2)-0.00248+C_2+(S_{23}+S_3-\alpha_2)+0.00086+\alpha_7 \\ \delta_7=0.69+C_2+0.134+S_{23}-0.0238+S_2 \end{array}
  \delta_9 = \zeta_4 + 0.00248 * S_2 * \alpha_6
 \delta_{10} = \zeta_5 + 0.00248 * S_2 * \alpha_7

\delta_{11} = 0.5 * \zeta_7
 \delta_{12} = \zeta_7 * \chi_1 

\delta_{13} = -0.00248 * C_3 * \alpha_5
 \delta_{14} = \delta_{13} + \chi_2

\delta_{15} = 0.5 + (\delta_5 + \beta_1 + \delta_{13} + \beta_6)
 \begin{array}{l} \delta_{16} = \zeta_9 * \chi_3 \\ \delta_{17} = 0.5 * (\delta_6 * \beta_1 + \zeta_9 * \beta_6) \end{array}
  δ<sub>18</sub> = ζ<sub>4</sub>
 \delta_{19}^{18} = \zeta_{5}^{4}
\delta_{20}^{2} = -0.0025 * \alpha_{7}
\delta_{21}^{2} = -0.00125 * \alpha_{6}^{2} * (\beta_{13} + \beta_{15})
  \delta_{22} = -0.000642 * S_{23} * C_4
  \delta_{23} = \zeta_3 * \chi_3
 \delta_{24} = \zeta_3 * \chi_1 

\delta_{25} = 0.000642 * S_4
  \delta_{26} = \delta_{25} * \chi_3

\delta_{27} = -\delta_{25} * \chi_2
  Gravitational Forces/Torques
\begin{aligned} G[2] &= -37.2 * C_2 + \zeta_1 + 1.02 * S_2 \\ G[3] &= \zeta_1 + 0.25 * C_{23} \\ G[4] &= 0.028 * S_{23} * \alpha_5 \\ G[5] &= -0.028 * (C_{23} * S_5 + S_{23} * \alpha_9) \end{aligned}
```

### Joint Forces/Torques

```
\begin{split} T\{1\} &= D\{1,1\} \circ \bar{q}_1 + D\{1,2\} \circ \bar{q}_2 + D\{1,3\} \circ \bar{q}_3 + \delta_1 \circ \beta_2 + \delta_3 \circ \beta_3 + \delta_3 \circ \beta_4 + \delta_6 \circ \beta_3 + \delta_8 + \delta_7 \circ \beta_6 \\ T\{2\} &= D\{1,2\} \circ \bar{q}_1 + D\{2,2\} \circ \bar{q}_2 + D\{2,3\} \circ \bar{q}_3 + \delta_9 \circ \beta_4 + \delta_{10} \circ \beta_5 + \delta_{12} + \delta_{14} + \delta_{16} \cdot \delta_2 \circ \beta_1 + G\{2\} \\ T\{3\} &= D\{1,3\} \circ \bar{q}_1 + D\{2,3\} \circ \bar{q}_2 + D\{3,3\} \circ \bar{q}_3 + D\{3,4\} \circ \bar{q}_4 + D\{3,5\} \circ \bar{q}_5 + \delta_{18} \circ \beta_4 + \delta_{19} \circ \beta_5 + \delta_{23} + \delta_{20} \circ \beta_{14} \cdot \delta_4 \circ \beta_1 \cdot \delta_{11} \circ \beta_6 + \delta_{21} + G\{3\} \\ T\{4\} &= D\{3,4\} \circ \bar{q}_3 + D\{4,4\} \circ \bar{q}_4 - \delta_9 \circ \beta_2 - \delta_{18} \circ \beta_3 + \delta_{22} \circ \beta_5 + \delta_{26} \cdot \delta_{15} + G\{4\} \\ T\{5\} &= D\{3,5\} \circ \bar{q}_3 + D\{5,5\} \circ \bar{q}_3 - \delta_{10} \circ \beta_2 - \delta_{19} \circ \beta_3 - \delta_{22} \circ \beta_4 - \delta_{24} \cdot \delta_{27} \cdot \delta_{17} + G\{5\} \\ T\{6\} &= D\{6,6\} \circ \bar{q}_6 \\ Computational load: 11 sines/cosines, 89 additions and 150 multiplications Simplification criteria of the symbolic equations: 1% of the significantive value \\ \end{split}
```

 $S_i=Sin(q_i), C_i=Cos(q_i), S_{ii}=Sin(q_i+q_j), C_{ii}=Cos(q_i+q_j)$ 

system resources. M. Kircanski et al. [30] propose the computation of the inverse dynamics using a 4:2:1 multirate method for the acceleration, velocity and position dependent terms, respectively. In this sense, we can get lower computing rates the lower the bandwidth of each signal, without having a significant loss of precision. For the PUMA 560 simplified dynamic equations we have the distribution of operations shown in Table III. Therefore, measuring the average computational load  $L_{av}$  by the operations (sin/cos,  $\pm$ , \*) and weighting the real-time evaluation through the 4:2:1 multirate method we get:

$$L_{\min} < L_{av} < L_{\max}$$
 (1a)  
 $\{0, 15, 16\} < \{2.75, 42.25, 62.25\} < \{11, 89, 150\}$  (1b)

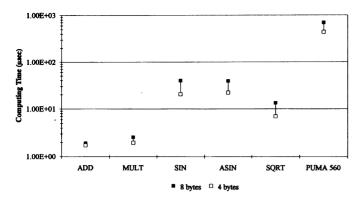


Fig. 3. Computing time  $\tau$  for several floating point operations in transputer T800–20 using 8 and 4 bytes precision (source code: Occam 2).

TABLE III
DISTRIBUTION OF OPERATIONS FOR THE 4:2:1 MULTIRATE METHOD

Operation	Acceleration Terms	Velocity Terms	Position Terms	Total Number of Terms
SIN/COS	0	0	11	11
ADD	15	35	39	89
MULT	16	51	83	150

where the lower  $L_{\min}$  and upper bound  $L_{\max}$  correspond to limit situations having the calculation of acceleration-dependent terms only and the total torques, respectively.

#### IV. CONCLUSIONS

A large number of controllers for robot manipulators have been proposed to date. However, the validation of these algorithms through practical implementations is still confined to a few examples. Moreover, many of these algorithms pose a high computational burden that precludes their industrial application using present day microcomputers. In order to overcome this situation, it is necessary to develop control strategies more adapted to microcomputer-based structures. The analysis of both the computational requirements and microcomputer capabilities reveals that some limitations are alleviated through the adoption of general techniques associated with data representation. Furthermore, the use of these techniques achieves not only a more efficient management of the computational resources but also provides a deeper insight on developments toward future control architectures.

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